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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission	11	Application No.	09/608,507
		Filing Date	June 30, 2000
		First Named Inventor	Salvador Palanca
		Group Art Unit	2186
		Examiner Name	Thai, T.
		Attorney Docket Number	42390P8918

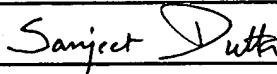
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ENCLOSURES (check all that apply)

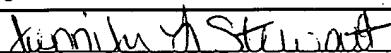
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Sanjeet K. Dutta, Reg. No. 46,145 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	July 26, 2002

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Attorney Docket No. 42390P8918

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re of Application of:

Salvador Palanca, et al.

Application No.: 09/608,507

Filing Date: June 30, 2000

For: METHOD AND APPARATUS FOR CACHE
REPLACEMENT FOR A MULTIPLE VARIABLE-
WAY ASSOCIATIVE CACHE

Examiner: Thai, T.

Art Group: 2186

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Commissioner for Patents
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AMENDMENT

Sir:

In response to the Office Action of May 3, 2002, please enter the following amendments.
Please cancel claims 17-21.

In the Claims

Sub
B1

1. A method comprising:
partitioning a cache array dynamically based upon requests for memory from an integrated device having a plurality of processors.
2. The method as claimed in claim 1, further comprising subdividing one or more ways within the cache array.

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